

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	radix near search\$3 and (lookup\$1 or reference\$1) same index\$3 and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:11
L2	5	radix near search\$3 and index\$3 and transition\$1 same bit\$1 same offset\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:12
S1	2	("6633880").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:44
S2	2	("5774739").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:46
S3	2	("6185570").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:47
S4	2	("5873078").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:51
S5	9075	707/2,102,104.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:52
S6	2	S5 and radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 11:54
S7	3	707/3,101.ccls. and radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:07
S8	5	radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:09

S9	10	radix near search\$3 and index\$3 same bit\$1 same key\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:10
S10	0	radix near search\$3 and index\$3 same base near address\$1 same (previous or old or prior) near index\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:11
S11	2	radix near search\$3 and index\$3 same (base\$1 or combin\$6) near address\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:12
S12	11	radix near search\$3 and index\$3 and result\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:14
S13	31	radix near search\$3 and index\$3 and (reference\$3 or lookup or look-up or pointer\$1) same bit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:46
S14	10	radix near search\$3 and index\$3 and (valid or validit\$3) near bit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:46
S15	11	radix near search\$3 and index\$3 and (valid or validit\$3) same bit\$1 same key\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:49
S16	1	radix near search\$3 and index\$3 and (offset with pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:03
S17	1	search\$3 and index\$3 same bit\$1 and (offset with pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:51
S18	1	search\$3 and index\$3 same bit\$1 and (offset same pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:51

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Time: 11:40:11

# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = MODELSKI

First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09741845</a>	Not Issued	71	12/22/2000	Route switch packet architecture	MODELSKI, RICHARD P.
<a href="#">09741846</a>	<a href="#">6981077</a>	150	12/22/2000	GLOBAL ACCESS BUS ARCHITECTURE	MODELSKI, RICHARD P.
<a href="#">09741852</a>	<a href="#">6772268</a>	150	12/22/2000	CENTRALIZED LOOK UP ENGINE ARCHITECTURE AND INTERFACE	MODELSKI, RICHARD P.
<a href="#">09741853</a>	Not Issued	61	12/22/2000	Method and apparatus for monitoring a network using statistical information stored in a memory entry	MODELSKI, RICHARD P.
<a href="#">09741855</a>	<a href="#">6684300</a>	150	12/22/2000	EXTENDED DOUBLE WORD ACCESSES	MODELSKI, RICHARD P.
<a href="#">09741856</a>	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	MODELSKI, RICHARD P.
<a href="#">09741857</a>	Not Issued	41	12/22/2000	Multi-thread packet processor	MODELSKI, RICHARD P.
<a href="#">09741858</a>	<a href="#">6847640</a>	150	12/22/2000	HIGH SPEED PHYSICAL CIRCUITS OF MEMORY INTERFACE	MODELSKI, RICHARD P.
<a href="#">09741859</a>	<a href="#">6633880</a>	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	MODELSKI, RICHARD P.
<a href="#">09741999</a>	Not Issued	93	12/22/2000	BIT FIELD MANIPULATION	MODELSKI, RICHARD P.
<a href="#">09742286</a>	<a href="#">6665755</a>	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	MODELSKI, RICHARD P.
<a href="#">09742287</a>	Not Issued	61	12/22/2000	Bi-directional access port architecture	MODELSKI, RICHARD P.
<a href="#">09742288</a>	Not Issued	71	12/22/2000	Load-shift carry instruction	MODELSKI, RICHARD P.
<a href="#">09742289</a>	Not	161	12/22/2000	Method and apparatus for key	MODELSKI,

	Issued			hashing based on a programmable hash function	RICHARD P.
<a href="#">09742290</a>	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	MODELSKI, RICHARD P.
<a href="#">09742300</a>	<a href="#">6778526</a>	150	12/22/2000	HIGH SPEED ACCESS BUS INTERFACE AND PROTOCOL	MODELSKI, RICHARD P.
<a href="#">10678677</a>	<a href="#">6934780</a>	150	10/03/2003	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	MODELSKI, RICHARD P.

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="MODELSKI"/>	<input type="text" value="RICHARD"/>	

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## Inventor Name Search Result

Your Search was:

Last Name = CRAREN

First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09741845</a>	Not Issued	71	12/22/2000	Route switch packet architecture	CRAREN, MICHAEL J.
<a href="#">09741846</a>	<a href="#">6981077</a>	150	12/22/2000	GLOBAL ACCESS BUS ARCHITECTURE	CRAREN, MICHAEL J.
<a href="#">09741856</a>	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	CRAREN, MICHAEL J.
<a href="#">09741857</a>	Not Issued	41	12/22/2000	Multi-thread packet processor	CRAREN, MICHAEL J.
<a href="#">09741859</a>	<a href="#">6633880</a>	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	CRAREN, MICHAEL J.
<a href="#">09741999</a>	Not Issued	93	12/22/2000	BIT FIELD MANIPULATION	CRAREN, MICHAEL J.
<a href="#">09742286</a>	<a href="#">6665755</a>	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	CRAREN, MICHAEL J.
<a href="#">09742288</a>	Not Issued	71	12/22/2000	Load-shift carry instruction	CRAREN, MICHAEL J.
<a href="#">09742290</a>	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	CRAREN, MICHAEL J.
<a href="#">09931643</a>	Not Issued	80	08/15/2001	Optical switch router	CRAREN, MICHAEL J.
<a href="#">10678677</a>	<a href="#">6934780</a>	150	10/03/2003	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	CRAREN, MICHAEL J.
<a href="#">60225594</a>	Not Issued	159	08/15/2000	Optical internet protocol switch router	CRAREN, MICHAEL J.

Inventor Search Completed: No Records to Display.

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = KRISTIANSEN

First Name = ADRIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09741852</a>	6772268	150	12/22/2000	CENTRALIZED LOOK UP ENGINE ARCHITECTURE AND INTERFACE	KRISTIANSEN, ADRIAN
<a href="#">09741853</a>	Not Issued	61	12/22/2000	Method and apparatus for monitoring a network using statistical information stored in a memory entry	KRISTIANSEN, ADRIAN M.
<a href="#">09741856</a>	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	KRISTIANSEN, ADRIAN M.
<a href="#">09741859</a>	6633880	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	KRISTIANSEN, ADRIAN M.
<a href="#">09742286</a>	6665755	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	KRISTIANSEN, ADRIAN M.
<a href="#">09742289</a>	Not Issued	161	12/22/2000	Method and apparatus for key hashing based on a programmable hash function	KRISTIANSEN, ADRIAN M.
<a href="#">09742290</a>	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	KRISTIANSEN, ADRIAN M.
<a href="#">10678677</a>	6934780	150	10/03/2003	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	KRISTIANSEN, ADRIAN M.

**Inventor Search Completed: No Records to Display.**

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 KRISTIANSEN
  ADRIAN

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**Inventor Name Search Result**

Your Search was:

Last Name = ANGLE

First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08490983</a>	<a href="#">5541920</a>	150	06/15/1995	METHOD AND APPARATUS FOR A DELAYED REPLACE MECHANISM FOR A STREAMING PACKET MODIFICATION ENGINE	ANGLE, RICHARD
<a href="#">09108468</a>	<a href="#">6185570</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION BITS AND FIELDS IN TRANSITION TABLES	ANGLE, RICHARD
<a href="#">09108469</a>	<a href="#">6247014</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING HASH LOOKUPS USING VALID BIT TABLES WITH POINTERS	ANGLE, RICHARD
<a href="#">09108711</a>	<a href="#">6223174</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING VALID BIT TABLES WITH POINTERS	ANGLE, RICHARD
<a href="#">09108751</a>	<a href="#">6233574</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION TABLES WITH POINTERS	ANGLE, RICHARD
<a href="#">09309471</a>	<a href="#">6601150</a>	150	05/10/1999	MEMORY MANAGEMENT TECHNIQUE FOR MAINTAINING PACKET ORDER IN A PACKET PROCESSING SYSTEM	ANGLE, RICHARD
<a href="#">09741859</a>	<a href="#">6633880</a>	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	ANGLE, RICHARD L.
<a href="#">09742290</a>	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	ANGLE, RICHARD L.
<a href="#">10234689</a>	<a href="#">6771596</a>	150	09/04/2002	BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	ANGLE, RICHARD L.
<a href="#">10242259</a>	Not	71	09/11/2002	Multicast and unicast scheduling	ANGLE, RICHARD



	Issued			for a network device	L.
<u>07482245</u>	Not Issued	161	02/20/1990	MULTIPROCESSOR COMPUTER SYSTEM WITH EACH PROCESSOR HAVING A BI-LEVEL CACHE	ANGLE, RICHARD L.
<u>08684224</u>	<u>5873078</u>	150	07/19/1996	RADIX TREE SEARCH LOGIC	ANGLE, RICHARD L.
<u>08690225</u>	<u>5857196</u>	150	07/19/1996	METHOD FOR STORING A TREE OF POTENTIAL KEYS IN A SPARSE TABLE	ANGLE, RICHARD L.
<u>08708895</u>	<u>6226687</u>	150	09/05/1996	METHOD AND APPARATUS FOR MAINTAINING AN ORDER OF DATA PACKETS	ANGLE, RICHARD L.
<u>08710736</u>	<u>5774739</u>	150	09/20/1996	USING A LOOKUP PROCESSOR TO SEARCH A TABLE OF KEYS WHOSE ENTIRES CONTAIN INSTRUCTION POINTER VALUES OF CODE TO EXECUTE IF KEY IS FOUND	ANGLE, RICHARD L.
<u>08716989</u>	<u>6209020</u>	250	09/20/1996	DISTRIBUTED PIPELINE MEMORY ARCHITECTURE FOR A COMPUTER SYSTEM	ANGLE, RICHARD L.
<u>08717323</u>	<u>5761506</u>	150	09/20/1996	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
<u>08717404</u>	<u>5848257</u>	150	09/20/1996	METHOD AND APPARATUS FOR MULTITASKING IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
<u>09062393</u>	<u>6272516</u>	150	04/17/1998	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
<u>09062787</u>	<u>5935235</u>	150	04/16/1998	METHOD FOR BRANCHING TO AN INSTRUCTION IN A COMPUTER PROGRAM AT A MEMORY ADDRESS POINTED TO BY A KEY IN A DATA STRUCTURE	ANGLE, RICHARD L.
<u>09311833</u>	<u>6628646</u>	150	05/14/1999	PROGRAMMABLE MULTICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
<u>09311834</u>	<u>6477169</u>	150	05/14/1999	MULTICAST AND UNICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
<u>09311942</u>	<u>6661788</u>	150	05/14/1999	MULTICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
<u>09311995</u>	<u>6519225</u>	150	05/14/1999	BACKPRESSURE MECHANISM	ANGLE, RICHARD

				FOR A NETWORK DEVICE	L.
<u>09434159</u>	<u>6633576</u>	150	11/04/1999	APPARATUS AND METHOD FOR INTERLEAVED PACKET STORAGE	ANGLE, RICHARD L.
<u>09457508</u>	<u>6422876</u>	150	12/08/1999	HIGH THROUGHPUT INTERCONNECTION SYSTEM USING ORTHOGONAL CONNECTORS	ANGLE, RICHARD L.
<u>06402415</u>	<u>4466321</u>	150	07/28/1982	SCRAGG MILL	ANGLE, RICHARD O.
<u>09574865</u>	<u>6726400</u>	150	05/19/2000	VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.
<u>09951080</u>	<u>6685387</u>	150	09/13/2001	JET BLAST RESISTANT VEHICLE ARRESTING BLOCKS, BEDS AND METHODS	ANGLEY, RICHARD D.
<u>10755163</u>	<u>6971817</u>	150	01/09/2004	JET BLAST RESISTANT VEHICLE ARRESTING BLOCKS, BEDS AND METHODS	ANGLEY, RICHARD D.
<u>11253333</u>	Not Issued	30	10/19/2005	Jet blast resistant vehicle arresting blocks, beds and methods	ANGLEY, RICHARD D.
<u>08796968</u>	<u>5789681</u>	150	02/07/1997	ARRESTING MATERIAL TEST APPARATUS AND METHODS	ANGLEY, RICHARD D.
<u>08796969</u>	<u>5902068</u>	150	02/07/1997	VEHICLE ARRESTING UNIT FABRIICATION METHODS	ANGLEY, RICHARD D.
<u>08796970</u>	<u>5885025</u>	150	02/07/1997	VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.
<u>09259730</u>	Not Issued	168	03/01/1999	VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.

Inventor Search Completed: No Records to Display.

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	ANGLE	RICHARD	

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**Inventor Name Search Result**

Your Search was:

Last Name = LADWIG

First Name = GEOFF

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08902396</a>	<a href="#">6047304</a>	150	07/29/1997	METHOD AND APPARATUS FOR PERFORMING LANE ARITHMETIC TO PERFORM NETWORK PROCESSING	LADWIG, GEOFF
<a href="#">09108468</a>	<a href="#">6185570</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION BITS AND FIELDS IN TRANSITION TABLES	LADWIG, GEOFF
<a href="#">09108469</a>	<a href="#">6247014</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING HASH LOOKUPS USING VALID BIT TABLES WITH POINTERS	LADWIG, GEOFF
<a href="#">09108711</a>	<a href="#">6223174</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING VALID BIT TABLES WITH POINTERS	LADWIG, GEOFF
<a href="#">09108751</a>	<a href="#">6233574</a>	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION TABLES WITH POINTERS	LADWIG, GEOFF
<a href="#">60137201</a>	Not Issued	159	06/02/1999	HIGH THROUGHPUT CHASSIS INTERCONNECTION SYSTEM	LADWIG, GEOFF
<a href="#">09741859</a>	<a href="#">6633880</a>	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	LADWIG, GEOFF B.
<a href="#">09742290</a>	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	LADWIG, GEOFF B.
<a href="#">08490983</a>	<a href="#">5541920</a>	150	06/15/1995	METHOD AND APPARATUS FOR A DELAYED REPLACE MECHANISM FOR A STREAMING PACKET	LADWIG, GEOFFREY

				MODIFICATION ENGINE	
<u>08710736</u>	<u>5774739</u>	150	09/20/1996	USING A LOOKUP PROCESSOR TO SEARCH A TABLE OF KEYS WHOSE ENTIRES CONTAIN INSTRUCTION POINTER VALUES OF CODE TO EXECUTE IF KEY IS FOUND	LADWIG, GEOFFREY B.
<u>09863043</u>	Not Issued	161	05/22/2001	Communication channel optimization using forward error correction statistics	LADWIG, GEOFFREY B.
<u>10234689</u>	<u>6771596</u>	150	09/04/2002	BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
<u>10242259</u>	Not Issued	71	09/11/2002	Multicast and unicast scheduling for a network device	LADWIG, GEOFFREY B.
<u>08684224</u>	<u>5873078</u>	150	07/19/1996	RADIX TREE SEARCH LOGIC	LADWIG, GEOFFREY B.
<u>08690225</u>	<u>5857196</u>	150	07/19/1996	METHOD FOR STORING A TREE OF POTENTIAL KEYS IN A SPARSE TABLE	LADWIG, GEOFFREY B.
<u>08708895</u>	<u>6226687</u>	150	09/05/1996	METHOD AND APPARATUS FOR MAINTAINING AN ORDER OF DATA PACKETS	LADWIG, GEOFFREY B.
<u>08716989</u>	<u>6209020</u>	250	09/20/1996	DISTRIBUTED PIPELINE MEMORY ARCHITECTURE FOR A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
<u>08717323</u>	<u>5761506</u>	150	09/20/1996	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
<u>08717404</u>	<u>5848257</u>	150	09/20/1996	METHOD AND APPARATUS FOR MULTITASKING IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
<u>09062393</u>	<u>6272516</u>	150	04/17/1998	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
<u>09062787</u>	<u>5935235</u>	150	04/16/1998	METHOD FOR BRANCHING TO AN INSTRUCTION IN A COMPUTER PROGRAM AT A MEMORY ADDRESS POINTED TO BY A KEY IN A DATA STRUCTURE	LADWIG, GEOFFREY B.
<u>09309471</u>	<u>6601150</u>	150	05/10/1999	MEMORY MANAGEMENT TECHNIQUE FOR MAINTAINING PACKET ORDER IN A PACKET PROCESSING SYSTEM	LADWIG, GEOFFREY B.

<a href="#">09311833</a>	<a href="#">6628646</a>	150	05/14/1999	PROGRAMMABLE MULTICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
<a href="#">09311834</a>	<a href="#">6477169</a>	150	05/14/1999	MULTICAST AND UNICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
<a href="#">09311942</a>	<a href="#">6661788</a>	150	05/14/1999	MULTICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
<a href="#">09311995</a>	<a href="#">6519225</a>	150	05/14/1999	BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
<a href="#">09434159</a>	<a href="#">6633576</a>	150	11/04/1999	APPARATUS AND METHOD FOR INTERLEAVED PACKET STORAGE	LADWIG, GEOFFREY B.
<a href="#">09457508</a>	<a href="#">6422876</a>	150	12/08/1999	HIGH THROUGHPUT INTERCONNECTION SYSTEM USING ORTHOGONAL CONNECTORS	LADWIG, GEOFFREY B.

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